

## Refine Search

### Search Results -

Terms	Documents
L6 and (capacitor near4 ("chip" or "die") near6 surface)	13

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L9



Refine Search

Recall Text



Clear

Interrupt

### Search History

 DATE: Friday, January 27, 2006   [Printable Copy](#)   [Create Case](#)

#### Set Name Query

side by side

#### Hit Count Set Name

result set

DB=USPT; PLUR=YES; OP=OR

<u>L9</u>	L6 and (capacitor near4 ("chip" or "die") near6 surface)	13	<u>L9</u>
<u>L8</u>	L7 and ("on chip" or "on die")	0	<u>L8</u>
<u>L7</u>	L6 and (capacitor near4 ("chip" or "die"))	37	<u>L7</u>
<u>L6</u>	L5 and noise	37	<u>L6</u>
<u>L5</u>	L4 same (terminal\$ near8 (power near5 ground))	47	<u>L5</u>
<u>L4</u>	capacitor near4 (chip or die)	9130	<u>L4</u>

DB=PGPB; PLUR=YES; OP=OR

<u>L3</u>	L1 and "guiding"	0	<u>L3</u>
<u>L2</u>	L1 and "guiding devices"	0	<u>L2</u>
<u>L1</u>	20040183079	1	<u>L1</u>

END OF SEARCH HISTORY